

Substitute Form PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-449002	Application No. 10/829,075
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Daniel Yellin et al.		
		Filing Date April 21, 2004	Group Art Unit 2133	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
/JT/	AA	2002/0194567	12/2002	Yellin et al.			
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Foreign Patent Documents or Published Foreign Patent Applications							
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							Yes
	AK						
	AL						
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	AN						
	AO						

Other Documents (include Author, Title, Date, and Place of Publication)		
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/JT/	AP	Bahl, L.R., et al., "Optimal Decoding of Linear Codes for Minimizing Symbol Error Rate", <i>IEEE Trans. Inform. Theory</i> , pp. 248-287, March 1974.
/JT/	AQ	Forney, G.D., "The Viterbi Algorithm", <i>Proc. IEEE</i> , 61(3):268-278, March 1973.
/JT/	AR	Smit, G., et al., "Mapping the SISO module of the Turbo decoder to a FPFA", <i>Proc. of Second International Symposium on Mobile Multimedia Systems & Applications (MMSA2000)</i> , pp. 165-172, November 2000.
/JT/	AS	Yeo, E., et al., "VLSI Architecture for Iterative Decoders in Magnetic Recording Channels", <i>IEEE Transactions on Magnetics</i> , 37(2):748-755, March 2000.
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Examiner Signature /Joseph Torres/	Date Considered 4/25/2007
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	